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Patent No. : 6,821,821 *B2*

Patent Issued : November 23, 2004

Inventor(s) : Joseph Fjeistad

Title : METHODS FOR MANUFACTURING RESISTORS USING A SCARIFICIAL  
LAYER

Re: Request for Certificate of Correction

Consideration has been given your request for the issuance of a certificate of correction for the above-identified patent.

Respecting the alleged errors noted in your request, column 3, lines 21-22 is printed in accordance with the record.

In view of the foregoing your request in this matter is hereby denied.

A certificate of correction will issue for the remaining error noted in your request.

Further consideration will be given upon receipt of a Request for Reconsideration, which should be directed to Decisions and Certificate of Correction Branch. Requests for Reconsideration should be accompanied by additional support (e.g. copy of amendments, post card receipts. PTOL 1449 or 892, etc.), containing requested data or changes) and/or brief statements of facts, as requested.

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